

**Data Structures Added in the  
Berkeley Virtual Memory Extensions to the  
UNIX<sup>†</sup> Operating System<sup>‡</sup>**

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*ABSTRACT*

This document describes the major new data structures that have been introduced to the Version 7 UNIX system to support demand paging on the VAX<sup>\*</sup>-11/780. The reader should be basically familiar with the VAX architecture, as described in the *VAX-11/780 Hardware Handbook*.

When relevant, along with the data structures, we present related system constants and macro definitions, and some indications of how the data is used by the system algorithms. We also describe the extensions/deletions that have been made to some of the existing data structures. Full description of the paging system algorithms, however, is not given here.

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<sup>†</sup> UNIX is a Trademark of Bell Laboratories

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# Data Structures Added in the Berkeley Virtual Memory Extensions to the UNIX<sup>†</sup> Operating System<sup>‡</sup>

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## Introduction

The paging subsystem of the virtual memory extension to the system maintains four new, basic data structures: the (*system and process*) *page tables*, the *kernel map*, the *core map* and the *disk map*. This document consists of a description of each of these data structures in turn.

## PAGE TABLES

The format of the process page tables are defined in the system header file **pte.h**.<sup>†</sup> The basic form of the Page Table Entry (PTE) is dictated by the VAX-11/780 architecture. Both the first level page table, known as *Sysmap*, and the second level per-process page tables consist of arrays of this structure. The paging system makes use of several bit fields which have no meaning to the hardware. The individual fields are:

<b>pg_prot</b>	The <i>Protection Bits</i> . Define the access mode of the corresponding page. Modes used by the paging system include PG_NOACC for invalid entries, PG_KR for the text of the kernel, PG_KW for the kernel data space, PG_URKR for text portions of user processes, PG_URKW for text portions of user processes during modification (old form <i>exec</i> , and <i>ptrace</i> ) of text images, and PG_UW for normal user data pages.
<b>pg_m</b>	The <i>Modify Bit</i> . Set by hardware as a result of a write access to the page. Examined and altered by the paging subsystem to find out if a page is <i>dirty</i> and has to be written back to disk before the page frame can be released.
<b>pg_swapm</b>	Indicates whether the page has been initialized, but never written to the swapping area. This bit is necessary because <b>pg_m</b> is normally or'ed with <b>pg_vreadm</b> to see if the page has ever been modified, and thus <b>pg_m</b> is unavailable to force a swap in this case.
<b>pg_vreadm</b>	Indicates whether the page has been modified since it was last initialized. (Initialization occurs during stack growth, growth due to <i>break</i> system calls, and as a result of <i>exec</i> and <i>vread</i> system calls.) For use by the <i>vwrite</i> system call, which looks at the inclusive-or of this and the <b>pg_m</b> bit. A <i>vwrite</i> also clears this bit.

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<sup>†</sup> Copies of the system definitions (header) files related to the paging subsystem appear at the end of this document.

<b>pg_fod</b>	The <i>Fill on Demand</i> Bit. Set only when the valid bit (described below) is reset, indicating that the page has not yet been initialized. When referenced, the page will either be zero filled, or filled with a block from the file system based on other fields described next.
<b>pg_fileno</b>	Meaningful only when the <i>pg_fod</i> bit is set. If the <i>pg_fileno</i> field has value PG_FZERO, then a reference to such a page results in the allocation and clearing of a page frame rather than a disk transfer. When stack or data segment growth occurs, the page table entries are initialized to fill on demand pages with PG_FZERO in their <i>pg_fileno</i> fields. The page is otherwise filled in from the file system, either from the inode associated with the currently executing process (if <i>pg_fileno</i> is PG_FTEXT), or from a file.
<b>pg_blkno</b>	Gives the block number, in a file system determined by the value of the <i>pg_fileno</i> field, from which the page is to be initialized. Note that this is the logical block number in the file system, not in the mapped object. Thus no mapping is required at page fault time to locate the actual disk block; the system simply uses the <i>pg_fileno</i> field to locate an inode and uses the <i>i_dev</i> field of that inode in a call to a device strategy routine. The size of this field (20 bits) limits the maximum size of a filesystem to $2^{20}$ blocks (1M block).
<b>pg_v</b>	The <i>Valid</i> bit. Set only when the <i>pg_fod</i> bit is reset. Indicates that the mapping established in the PTE is valid and can be used by the hardware for address translation. Access to the PTE when this bit is reset causes an <i>Address Translation Not Valid</i> fault and triggers the paging mechanism. If both the valid and fill on demand bits are reset, but the <i>pg_pfnnum</i> field is non-zero, then the page is still in memory and can be reclaimed either from the loop or the free list without an I/O operation by simply revalidating the page, after possibly removing it from the free list. If the <i>pg_fod</i> bit is not set, and the <i>pg_pfnnum</i> field is zero, then the page has to be retrieved from disk. Note that resetting the valid bit for pages which are still resident allows for software detection and recording of references to pages, simulating a <i>reference</i> bit, which the VAX hardware does not provide.
<b>pg_pfnnum</b>	The <i>Page Frame Number</i> . Meaningful only when the <i>pg_fod</i> bit is reset. If the page frame is valid, then this gives the physical page frame number that the virtual page is currently mapped to. If no physical page frame is currently allocated this field is zero (except in page table entries in <i>Sysmap</i> , where unused entries are not always cleared.)

## System Page Tables

The first level page table *Sysmap* consists of a physically contiguous array of PTEs defined by the processor registers SBR (System Base Register), and SLR (System Length Register). SLR is loaded with the constant *Syssize* at system initialization and remains fixed thereafter.

The first four pages of the *Sysmap* map the kernel virtual memory from addresses 0x80000000 to the end of kernel data space onto the first pages of physical memory. Four pages is enough to map a kernel supporting a full load of memory on a VAX-11/780. Immediately after the pages which map the kernel text and data are the entries which map the user structure of the current running process (the *u*. area.) The *u*. area is currently six pages long, with the first two of these pages mapping the *user* structure itself, and the other four pages mapping the per-process kernel stack.† The position of the *u*. in *Sysmap* determines that it will live, in this system, at 0x80040000.

After the map entries reserved for the *u*. area are 16 words of system map reserved for utility usage. The *copyseg* routine uses one of these (CMAP2) while making a copy of one data page to map the destination page frame. This is necessary because at the point of copying (during the *fork* system call) the parent

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† Currently all six pages are allocated physical memory; it is planned that in the future, the third of these six pages will be made a read-only copy of *zeropage*. Since the stack is observed rarely to enter the third page this will leave a full page for unanticipated worst-case stack growth, and give a clean termination condition should the stack ever accidentally grow beyond three pages.

process is running, while the destination page is not in the parents address space, but rather destined for the child's address space. Since the parent may fault on the source page during the copy, the contents of this map are saved in the software extension to the *pcb* during context switch. Other utilities are used by *clearseg* to map pages to be cleared in implementing zero fill on demand pages, and by the **mem.c** driver to map pages in */dev/mem* when accessing physical memory.

The *Sysmap* continues with sets of entries for the UBA control and map registers, the physical device memory of a UNIBUS adaptor, and the control and map registers of upto three MASSBUSS adaptors. Each of these consists of 16 page table entries, mapping 8K bytes.

Next, there are a set of map entries for manipulating *u.* structures other than the one of the current running process. For instance, the page out demon, which runs as process 2, needs access to the diskmap information of a process whose page is being written to the disk. To get access to this, it uses six entries in the *Sysmap*, known as *Pushmap*, to map this *u.* into kernel virtual memory at a virtual address given by *pushutl*. There are several other map/utl pairs: *Swapmap* and *swaputl*, *Xswapmap* and *xswaputl*, *Xswap2map* and *xswap2utl*, *Forkmap* and *forkutl*, *Vfmap* and *vfutl*. These are used in swapping and forking new processes.

The final portion of the *Sysmap* consists of a map/utl like pair *Usrptmap/usrpt* which is a resource allocated to hold the first level page tables of all currently core-resident processes. This is a very important structure and will be described after we describe the basic structure of the page tables of a process.

### Per-process page tables

Each process possesses three logical page tables: one to map each of the text, data and stack segments. Large portions of the system refer to page table entries in each of these segments by an index, with the first page of each segment being numbered 0.

For the VAX-11/780 version of the system, these page tables are implemented by two physically distinct page tables, the *P0 Page Table*, mapping the text and data segments, and the *P1 Page Table*, mapping the stack segment. Within the P0 region, the text segment is mapped starting at virtual address 0 with the data segment following on the first page boundary after it.\* The stack segment, on the other hand, starts at the bottom of the P1 region and grows toward smaller virtual addresses. The constant *USRSTACK* corresponds to the address of the byte one beyond the user stack. The process page tables are contiguous in kernel virtual memory (KVM) and are situated with the P0 table followed by the P1 table such that the top of the first and the bottom of the second are aligned at page boundaries. Note that this results in a *gap* between the two page tables whose size does not normally exceed one page.

The size of the process' page tables (P0 + gap + P1) in pages is contained in the software extension to the *pcb* located at the top of the process' *u.* area (in *u\_pcb.pcb\_szpt*). This number is also duplicated in the *proc* structure field *p\_szpt*.

Given *x* as the virtual size of a process, **ctopt(x)** results in the minimum number of pages of page tables required to map it. A process accesses its page tables through the descriptors *P0BR*, *P0LR*, *P1BR*, and *P1LR*. The per-process copies of these processor registers are contained in the *pcb* and are loaded and saved at process context switch time. A copy of the P0 region base register is contained in the *proc* structure field *p\_p0br*.

Given the above description of the process layout in virtual memory, a pointer to a process and a page table entry, the **isa?pte** macros result in *true* if the PTE is within the respective segment of process *p*:

<b>isaspte(p, pte)</b>	stack segment?
<b>isatpte(p, pte)</b>	text segment?
<b>isadpte(p, pte)</b>	data segment?

Conversion between segment page numbers and pointers to page table entries can be achieved by the following macros, where *p* is the process of interest, and *i* is the virtual page number within the segment (a

\*Later versions of the system for the VAX-11/780 may align the data starting at a 64K byte boundary, i.e. each of the text, data and stack segments will use an integral number of first level (*Sysmap*) entries. There would then be a minimum of one page of page tables for each segment, and sharing of text page table pages will be made simple using the ability of the first level entries to point to common page table pages.

non-negative integer). These are used in dealing with the *core map* where the page numbers are kept in this form for compactness.

<b>tptopte(p, i)</b>	text page number to pte
<b>dptopte(p, i)</b>	data page number to pte
<b>sptopte(p, i)</b>	stack page number to pte

The VAX hardware also supports a virtual page frame number. These begin at 0 for the first page of the P0 region and increase through the text and data regions. For the stack region they begin at the frame before *btov(USRSTACK)* and decrease. Note that the first stack page has a large (but positive) virtual page frame number.

Page frame numbers in the system are very machine dependent, and are referred to as “v”s. The function **vtopte(p, v)** will take a *v* for a given process *p* and give back a pointer to the corresponding page table entry. The function **ptetov(p, pte)** performs the inverse operation. To decide which segment a pte is in, and to thereafter convert from pte’s to segment indices and back, the following macros can be used:

<b>isatsv(p, v)</b>	is v in the text segment of process p?
<b>isadsv(p, v)</b>	is v in the data segment of process p?
<b>isassv(p, v)</b>	is v in the stack segment of process p?
<b>vtotp(p, v)</b>	segment page number of page v, which is in text
<b>vtodp(p, v)</b>	segment page number of page v, which is in data
<b>vtosp(p, v)</b>	segment page number of page v, which is in stack
<b>tptov(p, i)</b>	v of i’th text page
<b>dptov(p, i)</b>	v of i’th data page
<b>sptov(p, i)</b>	v of i’th stack page
<b>ptetotp(p, pte)</b>	pte to a text segment page number
<b>ptetodp(p, pte)</b>	pte to a data segment page number
<b>ptetossp(p, pte)</b>	pte to a stack segment page number
<b>tptopte(p, i)</b>	pte pointer for i’th text page
<b>dptopte(p, i)</b>	pte pointer for i’th data page
<b>sptopte(p, i)</b>	pte pointer for i’th stack page

The functions *vtopte* and *ptetov* have trivial definitions in terms of these macros.

### Page table entries as integers

In a few places in the kernel, it is convenient to deal with page table entry fields *en masse*. In this case we cast pointers to page table entries to be pointers to integers and deal with the bits of the page table entry in parallel. Thus

```
struct pte *pte;

*(int *)pte = PG_UW;
```

clears a page table entry to have only an access field allowing user writes, by referencing it as an integer. When accessing the page table entry in this way, we use the manifest constant declarations in the *pte.h* file which give us the appropriate bits.

## THE KERNEL MAP

Defined in *map.h*. The kernel map is used to manage the portion of kernel virtual memory (KVM) allocated to mapping page tables of those processes that are currently loaded. On the VAX-11/780 this involves managing page table entries in the first level page table, in the *Usrptmap/usrpt* portion of the *Sysmap*. The size of the KVM devoted to mapping resident process page tables is set by *USRPTSIZE* in number of *Sysmap* entries. Note that this allows the mapping of a maximum of 64K \* *USRPTSIZE* bytes of resident user virtual address space. The maximum can be achieved only if there is no fragmentation in

the allocation.

KVM required to map the page tables of a process that is being swapped in is allocated according to a *first fit* policy through a call to the standard system resource allocator *malloc*. Once a process is swapped in, its page tables remain stationary in KVM unless the process grows such that it requires additional pages of page tables. At that time, the process' page tables are moved to a new region of KVM that is large enough to contain them. Upon swap out, the process deallocates KVM required to map its page tables through a call to the standard resource release routine *mfree*.<sup>†</sup>

There are two macros which can be used for conversion between *Usrptmap* indices and kernel virtual addresses.

<b>kmxtob(a)</b>	converts <i>Usrptmap</i> index to virtual address
<b>btokmx(b)</b>	converts virtual address to <i>Usrptmap</i> index

## CORE MAP

The core map structure is defined in **cmaph.h**. Each entry of core map contains eight bytes of information consisting of the following fields:

<b>c_next</b>	Index to the next entry in the free list. The size of this field (14 bits) limits the number of page frames that can exist in the main memory to 16K (8M bytes).
<b>c_prev</b>	Index to the previous entry in the free list.
<b>c_page</b>	Virtual page number within the respective segment (text, data or stack). The size of this field (17 bits) limits the virtual size of a process segment to 128K pages (i.e., 64M bytes).
<b>c_ndx</b>	Index of the proc structure that owns this page frame. In the case of shared text, the index is that of the corresponding text structure. The size of this field (10 bits) limits the number of slots in the <i>proc</i> and <i>text</i> structures to 1024.
<b>c_intrans</b>	The intransit bit. Important only for shared text segment pages, but set for private data pages for purposes of post-mortem analysis. Indicates that a page-in operation for the corresponding page has already been initiated by another process. Causes the faulting process to enter a wait state until awakened by the process that initiated the transfer. (This is logically part of the <b>c_flag</b> field, and is separate because of alignment considerations in the coremap.)
<b>c_flag</b>	8 bits of flags.

The meanings of the flags are:

<b>MWANT</b>	The page frame has a process sleeping on it. The process to free it should perform a wakeup on the page.
<b>MLOCK</b>	Lock bit. The page frame is involved in raw I/O or page I/O and consequently unavailable for replacement.
<b>MFREE</b>	Free list bit. The page frame is currently in the free list.
<b>MGONE</b>	Indicates that the virtual page corresponding to this page frame has vanished due to either having been deallocated (contraction of the data segment) or swapped out. The page will eventually be freed by the process which is holding it, usually the page-out demon.
<b>MSYS</b>	System page bit. The page frame has been allocated to a user process' <i>u</i> . area or page tables and therefore unavailable for replacement.
<b>MSTACK</b>	Page frame belongs to a stack segment.

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<sup>†</sup> Due to the way in which *malloc* works, the KVM mapped by the first entry in *Usrptmap* (index 0) is not used.

**MDATA** Page frame belongs to a data segment.

**MTEXT** Page frame belongs to a shared text segment.

The core map is the central data base for the paging subsystem. It consists of an array of these structures, one entry for each page frame in the main memory excluding those allocated to kernel text and data.

The memory free list, managed by **memall** and **memfree** is created by doubly linking entries in core map. The reverse link is provided to speed up page reclaims from the free list which have to perform an unlink operation.

There are a pair of macros for converting between core map indices and page frame numbers, since no core map entries exist for the system.

<b>cmtopg(x)</b>	converts core map index <i>x</i> to a page frame number
<b>pgtocm(x)</b>	converts a page frame number to a core map index

The macros for manipulating segment page numbers, which we described in the section on page tables above, are very useful when dealing with the core map.

## DISK MAP

Defined in *dmap.h*. The disk map is a per-process data structure that is kept in the process *u*. area. The fields are:

<b>dm_size</b>	The amount of disk space allocated that is actually used by the segment.
<b>dm_alloc</b>	The amount of physical disk space that is allocated to the segment.
<b>dm_dmap</b>	An array of disk block numbers marking the beginning of disk areas that constitute the segment disk image.

The four instances of the disk map allow the mapping of process virtual addresses to disk addresses for the parent data, parent stack, child data, and child stack segments. The two child maps are used during the *fork* system call serving to make both the parent and the child disk images accessible simultaneously.†

Each entry in the disk map array contains a disk block number (relative to the beginning of the swap area) that marks the beginning of a disk area mapping the corresponding segment of virtual space. The initial creation of the segment results in DMMIN blocks (512 bytes each) pointed to by the first disk map entry to be allocated. These disk blocks map precisely to the first DMMIN virtual pages of the corresponding segment. Subsequent growth of the segment beyond this size results in the allocation of 2\*DMMIN blocks mapping segment virtual page numbers DMMIN through 3\*DMMIN-1. This doubling process continues until the segment reaches a size such that the next disk area allocated has size DMMAX blocks. Beyond this point, the segment receives DMMAX additional blocks should it require them. Limiting the exponential growth at this size is in an effort to reduce severe disk fragmentation that would otherwise result for very large segments.

Note that increasing entries in the array map increasing segment virtual page numbers. However, in the case of the stack segment, this actually means mapping *decreasing* process virtual page numbers. Also note that since a shared text segment is static in size, its disk image is allocated in one contiguous block that is described by the text structure fields *x\_daddr* and *x\_size*.

The maximum size (in pages) that a segment can grow is determined by MAXTSIZ, MAXDSIZ, or MAXSSIZ for text, data, or stack segment respectively. Since the procedures that deal with the disk map panic on segment length overrun, setting the maximum size of a segment to a value greater than that can be mapped by its disk map can lead to a system failure. To avoid such a situation, the disk map parameters should be set so that possible segment overgrowth will be detected at an earlier time in life of a process by **chksize**. Note that the maximum segment size that can be mapped by disk map can be increased through raising any one or more of the constants NDMIN, DMMAX, and NDMAP.

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† These could actually be located on the kernel stack, rather than in the *u*. area.

## INSTRUMENTATION RELATED STRUCTURES

Currently, the system maintains counters for various paging related events that are accumulated and averaged at discrete points in time. The basic structure as defined in *vm.h* has the following fields:

<b>v_swpin</b>	Process swap ins.
<b>v_swpout</b>	Process swap outs.
<b>v_pswpin</b>	Pages swapped in.
<b>v_pswpout</b>	Pages swapped out.
<b>v_pgin</b>	Page faults requiring disk I/O.
<b>v_pgout</b>	Dirty page writes.
<b>v_intrans</b>	Page faults on shared text segment pages that were found to be intransit.
<b>v_pgreg</b>	Page faults that were serviced by reclaiming the page from memory.
<b>v_exfod</b>	Fill on demand from file system of executable pages (text or data from demand initialized executables.)
<b>v_zfod</b>	Fill on demand type page faults which filled zeros.
<b>v_vrfod</b>	Fill on demand from file systems of pages mapped by <i>vread</i> .
<b>v_nexfod</b>	Number of pages set up for fill on demand from executed files.
<b>v_nzfod</b>	Number of pages set up for zero fill on demand.
<b>v_vrfod</b>	Number of pages set up for fill on demand with <i>vread</i> .
<b>v_pgfrec</b>	Pages reclaimed from the free list.
<b>v_faults</b>	Address translation faults, any one of the above categories.
<b>v_scan</b>	Page frames examined by the page demon.
<b>v_rev</b>	Revolutions around the loop by the page demon.
<b>v_dfree</b>	Pages freed by the page demon.
<b>v_swth</b>	Cpu context switches.

The three instances of this structure under the names of *cnt*, *rate*, and *sum* serve the following purposes:

<b>cnt</b>	Incremental counters for the above events.
<b>rate</b>	The moving averages for the above events that are updated at various integral clock tick periods. The relevant macro for this operation is <b>ave(smooth, cnt, time)</b> which averages the incremental count <i>cnt</i> into <i>smooth</i> with aging factor <i>time</i> .
<b>sum</b>	Accumulated totals for the above events since reboot.

## EXISTING DATA STRUCTURES

Here we describe fields within existing data structures that have been newly introduced or have taken a new meaning.

### The Process Structure

<b>p_slptime</b>	Clock ticks since last sleep.
<b>p_szpt</b>	Number of pages of page table. This field is a copy of the <i>pcb_szpt</i> field in the <i>pcb</i> structure.
<b>p_tsize</b>	Text segment size in pages. This is a copy of the <i>x_size</i> field in the text structure.
<b>p_dsize</b>	Data segment size in pages.



<b>p_ssize</b>	Stack segment size in pages.
<b>p_rssize</b>	The current private segment (data + stack) <i>resident set size</i> for the process. The resident set is defined as the set of pages owned by the process that are either valid or reclaimable but not in the free list.
<b>p_swrss</b>	The size of the resident set at time of last swap out.
<b>p_p0br</b>	Pointer to the base of the P0 region page table. This is a copy of the <i>pcb_p0br</i> field in the <i>pcb</i> structure.
<b>p_xlink</b>	Pointer to another <i>proc</i> structure that is currently loaded and linked to the same text segment. The head of this linked list of such processes is contained in the text structure field <i>x_caddr</i> . Since the shared text portion of the process page tables are duplicated for each resident process attached to the same text segment, modifications to any one are reflected in all of them by sequentially updating the page table of each process that is on this linked list.†
<b>p_poip</b>	Count of number of page outs in progress on this process. If non-zero, prevents the process from being swapped in.
<b>p_faults</b>	Incremental number of page faults taken by the process that resulted in disk I/O.
<b>p_aveflt</b>	Moving average of above field.
<b>p_ndx</b>	Index of the process slot on behalf of which memory is to be allocated. During <i>vfork</i> , the memory of a process will be given to a child, but the reverse entries in <i>cmap</i> must still point to the original process so that the reverse links will point there when the <i>vfork</i> completes. This field thus indicates the original owner of the current process' virtual memory.

The new bits in the *p\_flag* field are:

<b>SSYS</b>	The swapper or the page demon process.
<b>SLOCK</b>	Process being swapped out.
<b>SSWAP</b>	Context to be restored from <i>u_ssav</i> e upon resume.
<b>SPAGE</b>	Process in page wait state.
<b>SKEEP</b>	Prevents process from being swapped out. Set during the reading of the text segment from the inode during exec and process duplication in fork.
<b>SDLYU</b>	Delayed unlock of pages. Causes the pages of the process that are faulted in to remain locked, thus ineligible for replacement, until explicitly unlocked by the process.
<b>SWEXIT</b>	Process working on <i>exit</i> .
<b>SVFORK</b>	Indicates that this process is the child in a <i>vfork</i> context; i.e. that the virtual memory being used by this process actually belongs to another process.
<b>SVFDONE</b>	A handshaking flag for <i>vfork</i> .
<b>SNOVM</b>	The parent of a <i>vfork</i> . The process has no virtual memory during this time. While this bit is set, the <i>p_xlink</i> field points to the process to which the memory was given.

### The Text Structure

The new fields in the text structure are:

<b>x_caddr</b>	Points to the head of the linked list of <i>proc</i> structures of processes that are currently loaded and attached to this text segment.
<b>x_rssize</b>	The resident set size for this text segment.
<b>x_swrss</b>	The resident set size for this text segment at the time of last swap out.
<b>x_poip</b>	Count of number of page outs in progress on this text segment. If non-zero, prevents the process from being swapped in.

† Used slightly differently when otherwise unused during *vfork*, see **SNOVM** below.

## The User Area Structure

The per-process user area contains the *u*. structure as well as the kernel stack. It is mapped to a fixed kernel virtual address (starting at 0x80040000) at process context switch. The user area is swapped in and out of disk as a separate entity and is pointed to by the proc structure field *p\_swaddr* when not resident. The number of pages allocated for the process' user area and kernel stack is six pages (UPAGES), thus the base of the kernel stack for a process is 0x80040c00.

The new fields that have been added to the *u*. structure are the following:

### **u\_pcb.pcb\_cmap2**

Contains the copy of Sysmap entry CMAP2 at context switch time. This kernel virtual address space mapping is made part of the process context due to the operation of the process duplication code that implements fork. Briefly, the process duplication is accomplished by copying from parent process' virtual address space to the child's virtual address space by mapping it to kernel virtual memory through CMAP2. Since this can result in faulting in the parent's address space, thus causing a block and context switch, the mapping of the child memory in the kernel must be saved and restored before the process can resume.

<b>u_nswap</b>	Number of times the process has been swapped. Not yet maintained.
<b>u_majorflt</b>	Number of faults taken by the process that resulted in disk I/O.
<b>u_cnswap</b>	Number of times the children of this process have been swapped. Not yet maintained.
<b>u_cmajorflt</b>	Number of faults taken by the children of this process that resulted in disk I/O. Not yet maintained.
<b>u_minorflt</b>	Number of faults taken by the process that were reclaims.
<b>u_dmap</b>	The disk map for the data segment.
<b>u_smap</b>	The disk map for the stack segment.
<b>u_cdmmap</b>	The disk map for the child's data segment to be used during fork.
<b>u_csmap</b>	The disk map for the child's stack segment to be used during fork.
<b>u_stklim</b>	Limit of maximum stack growth. To be varied through system calls. Currently not implemented.
<b>u_wantcore</b>	Flag to cause core dump even if the process is very large. Set by a system call. Currently not implemented.
<b>u_vrpages</b>	An array with an element for each file descriptor. Gives the number of fill on demand page table entries which have this file as their <b>pg_fileno</b> . If the count is non-zero, then the file cannot be closed, either by <i>close</i> or implicitly by <i>dup2</i> .

## The Inode Structure.

One field was added to the inode structure to support the *vread* system call:

<b>i_vfdcnt</b>	This counts the number of file descriptors (fd's) that have pages mapping this file with <i>vread</i> . If the count is non-zero, then the file cannot be truncated.
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